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ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10050104	FILING DATE 01/18/2002	CLASS 438	SUBCLASS 217 ?	GAU 2812	EXAMINER Bratlieff, D.
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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

REPUBLIC OF KOREA 2001-25078 05/09/2001

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PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no SEC.890
ATTORNEY DOCKET NO		
TITLE : Self-contained semiconductor device manufacturing equipment having compact arrangement of load-lock and processing chambers		

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L(Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drawg.	Figs. Drawg.
			Print Fig.
Primary Examiner			
PREPARED FOR ISSUE		Application Examiner	
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